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X-Y-THETA-Z STAGE FOR MASKED ION BEAM LITHOGRAPHY(U)  
NAVAL OCEAN SYSTEMS CENTER SAN DIEGO CA  
J M REEDS ET AL. MAY 87

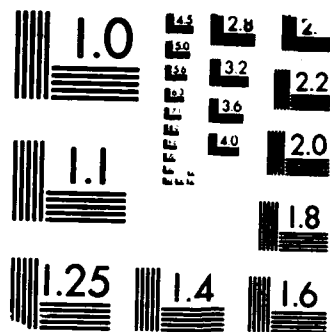
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MICROCOPY RESOLUTION TEST CHART  
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19. ABSTRACT (Continue on reverse if necessary and identify by block number) <p>A mechanical stage and electronic drive servos have been built which provide six degrees of freedom of motion in positioning. The stage is used in conjunction with a mask-wafer alignment sensing system to align a semiconductor wafer with a mask to 0.1 micrometer tolerance in a masked ion beam exposure system. Because the exposure system utilizes a step and repeat mode of operation, the stage system was designed to operate at high speed to achieve high wafer throughput. A backlash free capstan drive system is used on the X, Y, and <math>\theta</math> servos to achieve the high precision, high speed performance.</p> <p>The wafer to be exposed is mounted on top of an X-Y stage, which is, in turn, mounted on a rotary (<math>\theta</math>) stage, which is then mounted on a platform. The platform is supported from a base by three deformable diaphragms which allow limited vertical (Z) or tilt motion of the entire stage assembly. In step and repeat operation, the X and Y motions are relatively large from the chip-to-chip, while the <math>\theta</math> and Z motions are very small. Mounting the X-Y stage on the top of the stage assembly provides the design with the lowest mass to be accelerated in stepping, and has the additional advantage that the algorithms for providing mask-wafer alignment error signals remain the same no matter where the chip being aligned is located on the wafer. Dedicated microprocessors for each of the X, Y, and <math>\theta</math> axes, incorporating digital error integration, provide the required precision of motion.</p>			
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# X-Y- $\theta$ -Z STAGE FOR MASKED ION BEAM LITHOGRAPHY

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## ABSTRACT



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A mechanical stage and electronic drive servos have been built which provide six degrees of freedom of motion in positioning. The stage is used in conjunction with a mask-wafer alignment sensing system to align a semiconductor wafer with a mask to 0.1 micrometer tolerances in a masked ion beam exposure system. Because the exposure system utilizes a step and repeat mode of operation, the stage system was designed to operate at high speed to achieve high wafer throughput. A backlash free capstan drive system is used on the X, Y, and  $\theta$  servos to achieve the high precision, high speed performance.

The wafer to be exposed is mounted on top of an X-Y stage, which is, in turn, mounted on a rotary ( $\theta$ ) stage, which is then mounted on a platform. The platform is supported from a base by three deformable diaphragms which allow limited vertical (Z) or tilt motion of the entire stage assembly. In step and repeat operation, the X and Y motions are relatively large from chip to chip, while the  $\theta$  and Z motions are very small. Mounting the X-Y stage on the top of the stage assembly provides the design with the lowest mass to be accelerated in stepping, and has the additional advantage that the algorithms for providing mask-wafer alignment error signals remain the same no matter where the chip being aligned is located on the wafer. Dedicated microprocessors for each of the X, Y, and  $\theta$  axes, incorporating digital error integration, provide the required precision of motion.

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